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TDANGMITT	- Λ I	Application Number	10/769,510			
TRANSMITTAL FORM (to be used for all consepondence after initial filing)		Filing Date	January-30, 2004			
		First Named Inventor	Michael Eneboe			
		Group Art Unit	2814			
		Examiner Name	Siek, Vuthe			
Total number of pages in this submissi	30	Attorney Docket Number	er 01-490/1C			
	EN	CLOSURES (check all that	apply)			
Fee Transmittal Form Fee Attached	Assignment Drawing(s)	Papers	After Allowance Communication to Group Appeal Communication to Board of Appeals and			
Amendment/Response		elated Paper	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)			
After Final		nting Slip (PTO/SB/69) panying Petition	Proprietary Information			
Affidavits(s)/declaration(s To Convert a Provisional A		Application	Status Letter			
Corresponde Express Abandonment Reques Information Disclosure Statemen Small Entity		torney, Change of ence Address	Additional Enclosure(s) (please identify below):			
		sclaimer	Return address postcard for PTO mailroom to date			
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Certified Copy of Priority Document(s) Request for		Refund				
Response to Missing Parts/ Incomplete Application	Remarks					
Response to Missing Parts under 37 CFR 1.52 or 1.53	Parts under 37 CFR 1.52					
SIGNATU	IRE OF APPLI	CANT, ATTORNEY, OR	AGENT			
Individual Name Timothy Cro	oll, Reg. No	o. 36,771, Phone:	[+1] 408-433-7475			
Signature 25 R W						
Date 130 to 4						
CERTIFICATE OF MAILING						
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope to: Assistant Commissioner for Patents, Washington, D,C. 20231 on this date: CO / 14 / 04						
Typed or printed name Manu Kashyap, Phone: [+1] 408-433-7475						
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael Eneboe et. al.

Serial No.:

10/769,510

Filed:

January 30, 2004

OF ET COS

System and Method for Optimizing an Integrated Circuit Design

§ § §

Group Art Unit: 2814

Examiner:

Siek, Vuthe

Atty Docket:

/ 01-490/1C

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

Date

1/6K

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 130 to 4

Respectfully submitted,

Timothy Croll

Reg. No. 36,771

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Comp	elete if Known	
Application Number	10/769,510	
Filing Date	January-30, 2004	
First Named Inventor	Michael Eneboe	
Group Art Unit	2814	
Examiner Name	Vuthe Siek	
Attorney Docket No.	/ 01-490/1C	

U.S. PATENT DOCUMENTS				
Examiner Cite Initials No.	U.S. Patent Document Number Kind Code (if known)	Name of Patentee or Applicant	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	6,070,003	Gove et al.	05-30-2000	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	т	
		"SOP: A Reconfigurable Massively Parallel System and Its Control-Data-?Flow based Compiling Method", IEEE, pages: 148-156, 1996 - Author(s) - Yamauchi et al.		
		"Code Generation for Core Processors", ACM, pages, 232-237, 1997 - Author(s) - Marwedel		
	!	"Configurable Computing", Scientific American, http://www.sciam.com, pages: 1-10, 6-1997 - Author(s) - Villasenor et al.		
		"SOP: Adaptive Massively Parallel System", CiteSeer, pages: 1-2, 1995 - Author(s) - Yamauchi et al.		

Examiner	Date	
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